Electronic Pater	nt App	lication Fee	e i ransm	ittai			
Application Number:	10	10765481					
Filing Date:	27-	27-Jan-2004					
Title of Invention:		Method and apparatus for a two-step resists oft bake to prevent ILD outgassing during semiconductor processing					
First Named Inventor/Applicant Name:	Pai	Paul Shirley					
Filer:	Rol	Robert A. Manware					
Attorney Docket Number:	MI	MICS:0117 (02-1051)					
Filed as Large Entity	Ţ,						
Utility under 35 USC 111(a) Filing Fees							
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)		
Basic Filing:							
Pages:							
Claims:							
Miscellaneous-Filing:							
Petition:							
Patent-Appeals-and-Interference:							
Post-Allowance-and-Post-Issuance:							
Extension-of-Time:							
Extension - 1 month with \$0 paid		1251	1	130	130		

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)				
Miscellaneous:								
Request for continued examination	1801	1	810	810				
	Tot	940						